

RISC-V: The Road Ahead

RISC-V started as an academic project in another UC a few years ago. Today, it is on the path to becoming the dominant architecture across all computing platforms. In this talk, I'll briefly talk about the history and current state of RISC-V processor IPs from a market perspective. We will then go into what are the key reasons why RISC-V has been more successful than other ISAs in the past and how these factors will shape the processor market in the next few years. We will explore some interesting research opportunities in the age of open architecture.

Bilal Zafar

Co-founder & CEO
10xEngineers



Date: Friday, February 17, 2023
Time: 12:00 PM
Location: Zoom

Zoom Link: <https://ucm.edu/EECS21723>

Biography

Bilal Zafar is the co-founder & CEO of 10xEngineers, a design & verification services company specializing in design and verification of RISC-V processors, IPs and SoCs, and image processors hardware. Previously, Dr. Zafar was Principal Engineer at Qualcomm.

Dr. Zafar holds a PhD in Computer Engineering from the University of Southern California, Los Angeles